

RAVULA PREM KUMAR

SOFTWARE ENGINEER
TRAINEE

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EMAIL | premgoudravula@yahoo.
com
LOCATION | Bengaluru, INDIA
EXPERIENCE | 1 Year 0 Month

Key Skills

- C
- Embedded C
- Microcontroller
- Microprocessors
- UART
- I2C
- SPI
- RS232
- RS485
- Keil IDE
- Vs Code
- Debugging
- RTOS
- NXP
- Networking
- Ubuntu
- OS
- ESP32
- Arduino Uno
- Mqtt
- GIT
- LINUX

Profile Summary

To achieve high career growth through a continuous learning process. The main aim is to innovate and contribute to my short- and long-term goals as an entrepreneur by effectively using technological & managerial skills.

Work Experience

Software Engineer Trainee

Innominds Software

03/2022 - 03/2023

1 year of experience in C and Embedded C development.

Worked on the Nymi-connected worker solution, enhancing security, privacy, and user experience. Performed system connectivity, installation, and configuration of Nymi tools.

Integrated the Nymi platform to enable seamless authentication and access control.

Implemented Nymi Band technology for biometric-based continuous and multi-factor authentication (MFA).

Ensured secure and flexible workplace access for employees.

Education

B.Tech/B.E. - Electronics/Telecommunication
2022

Vaagdevi Engineering College, Warangal

Grade - 61%

- ARM
- GPIO
- IoT
- CAN
- STM32

Certification

- Advanced Diploma In Embedded Systems At Radar Technical Centre, Bengaluru

Languages

- TELUGU
- ENGLISH
- HINDI

Hobbies

- Social Working
- Traveling
- Web-Browsing

Social links

- www.linkedin.com/in/ravula-prem-kumar-614538216

12th

2018

Telangana , English

Grade - 77%

10th

2016

Telangana , Telugu

Grade - 85%

Projects

ESP-32 SMART BLUETOOTH LOCK

3 Months

ESP-32 SMART BLUETOOTH LOCK Many automated advanced locking system of a door has been developed and its popularly used in many places like commercial buildings and organization. Some of these automated doors locking system are based FOR THE SYSTEM

Linear Image Processing through FPGA

4 Months

The aim of our project is to interfacing memory with FPGA processor to do some image processing and in this we ae implementing linear and morphological image filtering algorithm FPGAS Developed